



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Patent Application of:)
ALBORE ET AL.)
Serial No. 10/820,462)
Confirmation No. 7256)
Filing Date: April 8, 2004)
For: METHOD FOR PATCHING ROM)
INSTRUCTIONS IN AN ELECTRONIC)
EMBEDDED SYSTEM INCLUDING AT)
LEAST A FURTHER MEMORY PORTION)

REQUEST FOR CORRECTION TO THE FILING RECEIPT


MS Missing Parts
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

The filing receipt for the subject patent application has been received and reviewed for accuracy. A discrepancy has been discovered. The filing receipt in error lists the address of the second inventor, Pasquale Vastano as "Santa Maria Capua (Caserta), Italy". The correct address is **Santa Maria Capua Vetere (Caserta), Italy.**

In view of the error noted, it is respectfully requested that a corrected filing receipt be issued in this application. If any fee is believed to be necessary, authorization is given to charge Deposit Account No. **01-0484.**

Respectfully submitted,


MICHAEL W. TAYLOR
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In Re Patent Application of:

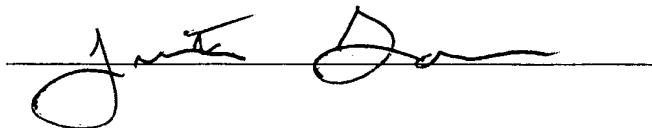
D'ALBORE ET AL.

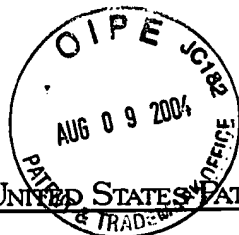
Serial No. **10/820,462**

Filing Date: **April 8, 2004**

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: MS MISSING PARTS, COMMISSIONER FOR PATENTS, P.O. BOX 1450, ALEXANDRIA, VA 22313-1450, on this 4th day of August, 2004.

A handwritten signature in black ink, appearing to be "John Doe", is written over a horizontal line.



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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/820,462	04/08/2004	2186	0.00	03MAR43253801	2	33	3

CONFIRMATION NO. 7256

27975

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FILING RECEIPT



OC000000013018866

Date Mailed: 06/22/2004

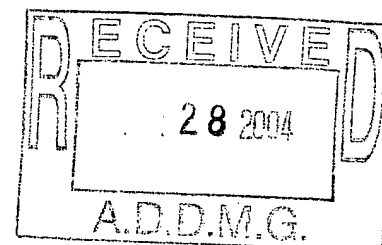
Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Guido D'Albore, Caserta, ITALY;
Pasquale Vastano, Santa Maria Capua (Caserta), ITALY;

Assignment For Published Patent Application

ST Incard S.r.l., Agrate Brianza (MI), ITALY;



Domestic Priority data as claimed by applicant

Foreign Applications

If Required, Foreign Filing License Granted: 06/21/2004

Projected Publication Date: To Be Determined - pending completion of Missing Parts

Non-Publication Request: No

Early Publication Request: No

Title

Method for patching ROM instructions in an electronic embedded system including at least a further memory portion

Preliminary Class

711

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Title 35, United States Code, Section 184
Title 37, Code of Federal Regulations, 5.11 & 5.15**

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